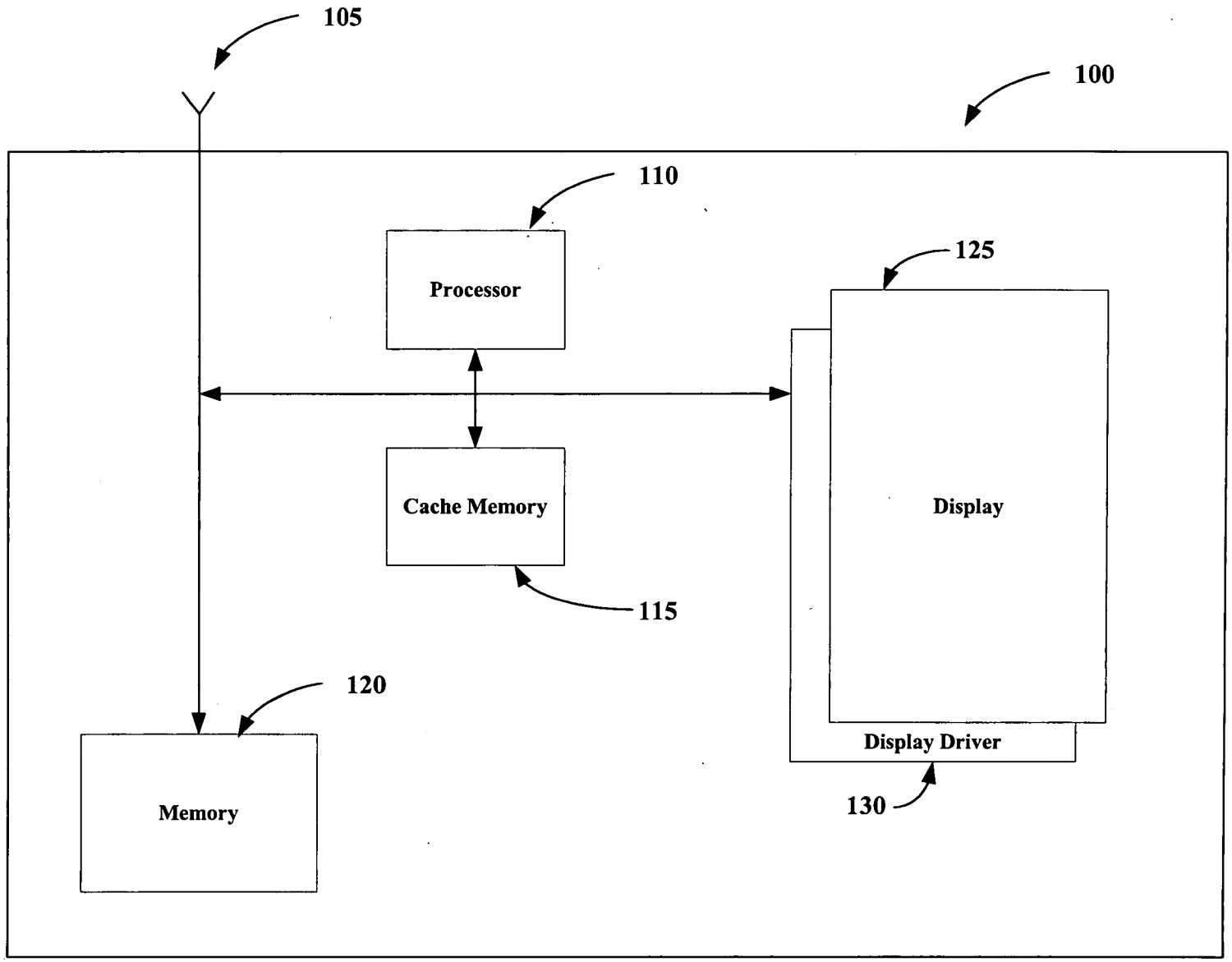
105
100
110
Processor
125
Display
Cache Memory
115
120
Memory
Display Driver
130

Figure 1

008260 - 5T85960

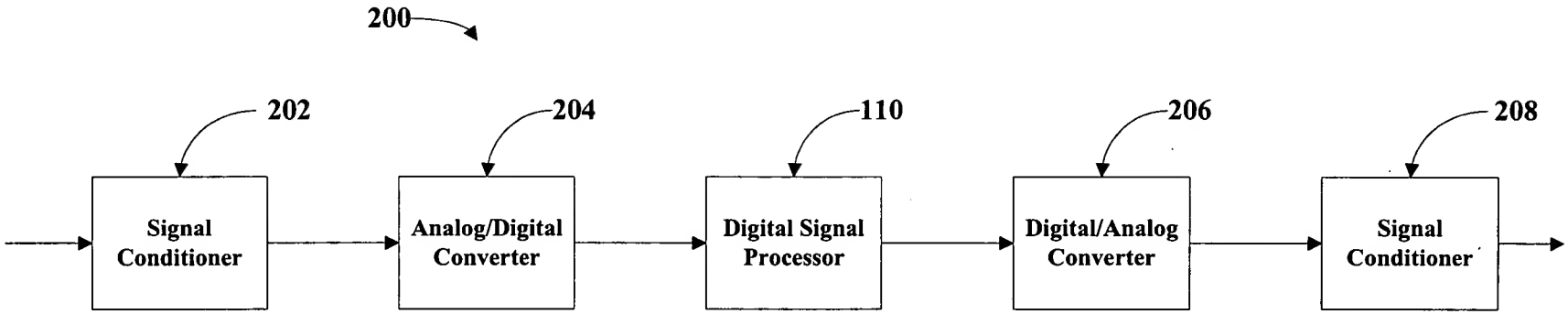

Figure 2

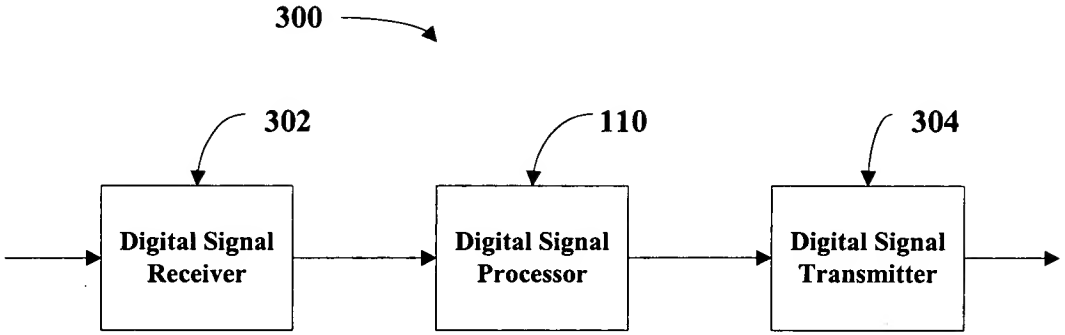

Figure 3

008260 "STB52960

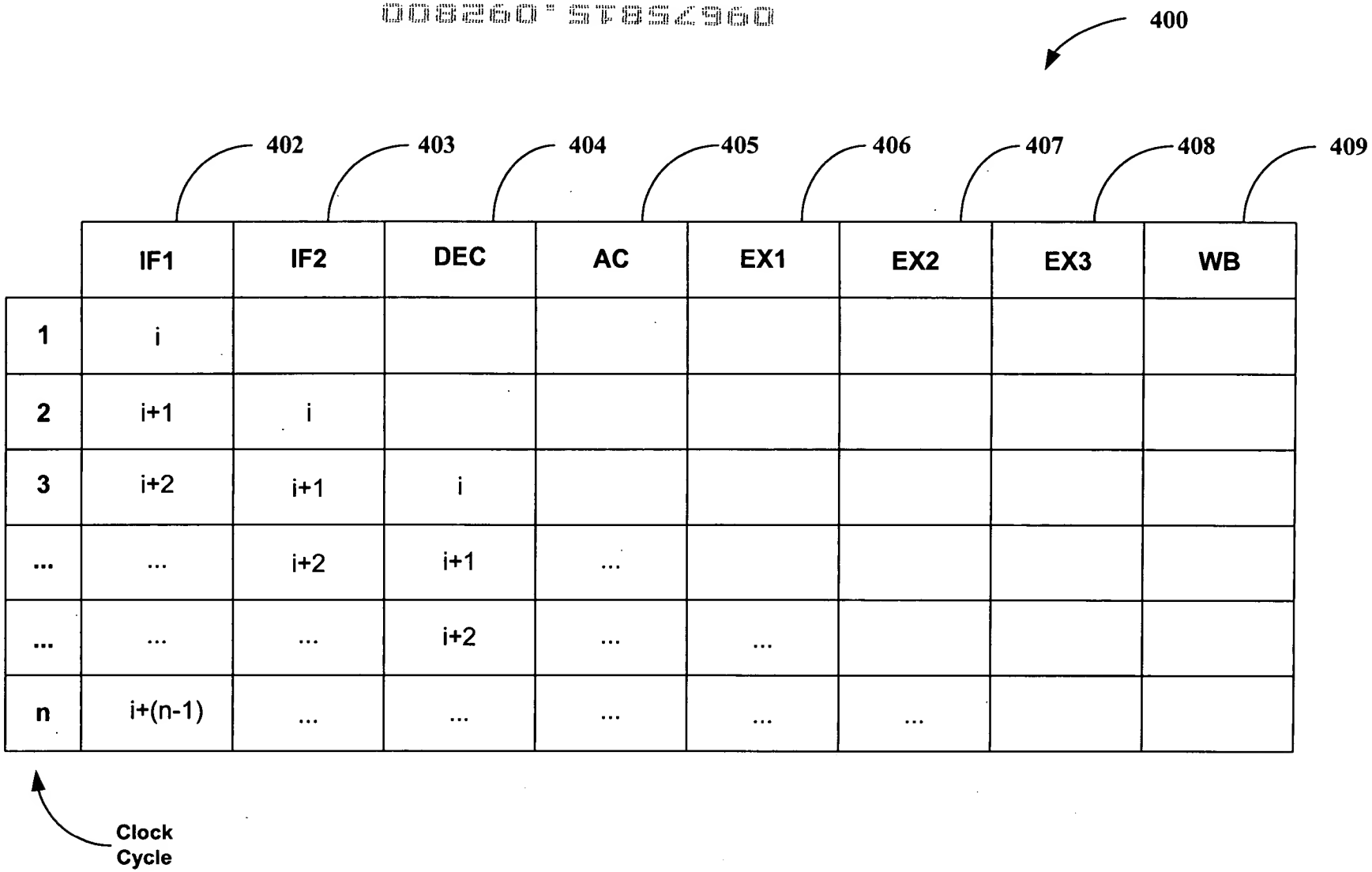

Figure 4

08260 ST85960

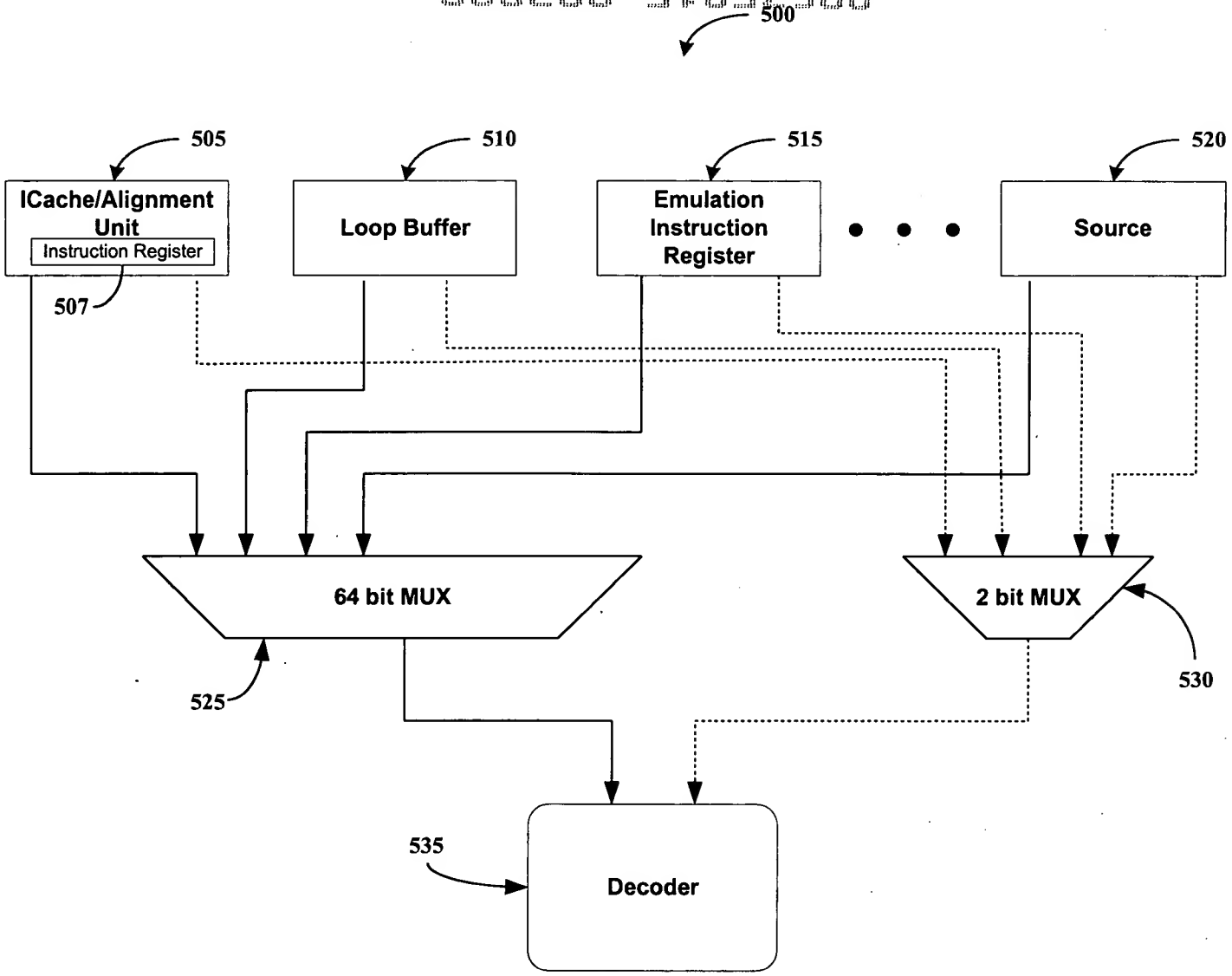

Figure 5

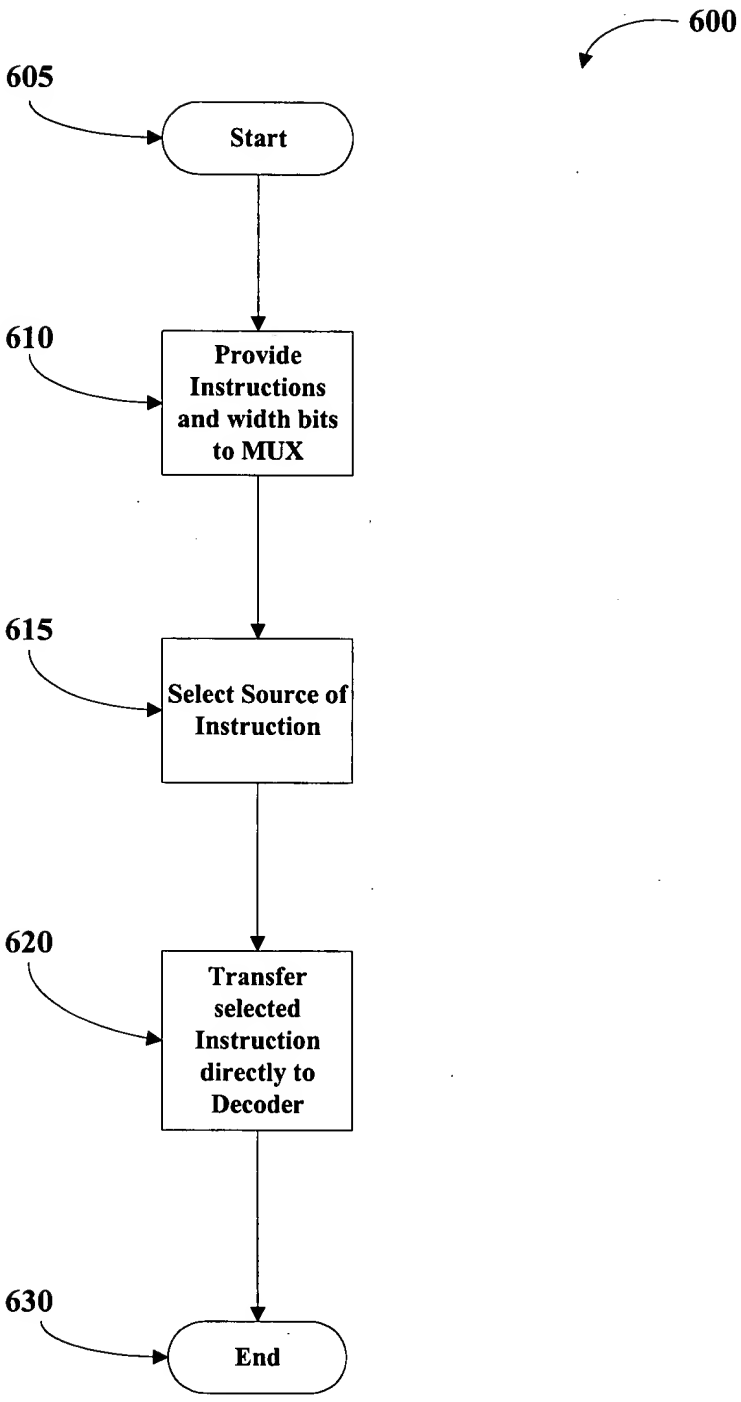
600
605
Start
610
Provide Instructions and width bits to MUX
615
Select Source of Instruction
620
Transfer selected Instruction directly to Decoder
630
End

Figure 6

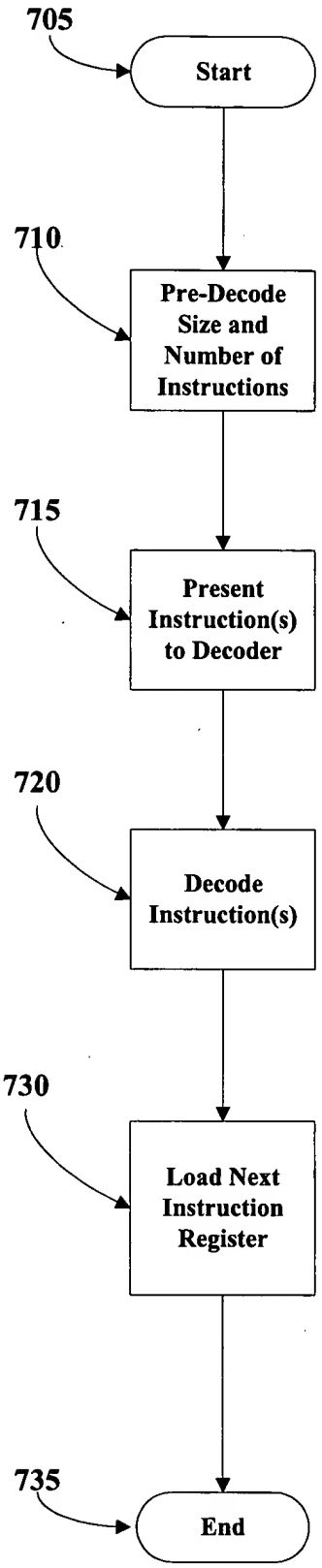
705
Start
700
710
Pre-Decode Size and Number of Instructions
715
Present Instruction(s) to Decoder
720
Decode Instruction(s)
730
Load Next Instruction Register
735
End

Figure 7